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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Meikei Ieong et al.

Examiner:

Tu Tu V. Ho

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For: CMOS ON HYBRID SUBSTRATE

. . . . _ _

Dated:

September 14, 2005

WITH DIFFERENT CRYSTAL

ORIENTATIONS USING SILICON-

TO-SILICON DIRECT WAFER BONDING

Confirmation No. 7287

Mailstop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated June 14, 2005, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent

and Trademark Office on the date shown below.

Dated: September 14, 2005

Leslie S. Szivos, Ph.D.